

AMENDMENTS TO THE CLAIMS

1. (currently amended) A liquid crystal display device, comprising:

first and second substrates that are divided in a display area and a non-display area;

a liquid crystal layer between the first and second substrates;

a plurality of switching devices on the first substrate;

first and second lines that apply signals to each switching device;

a plurality of first electrodes on the first substrate;

a first auxiliary line arranged in the non-display area and applying a voltage to the liquid crystal layer, the first auxiliary line receiving a first signal; and

a second auxiliary line arranged in the non-display area, the second auxiliary line receiving a second signal, ~~and~~

~~a liquid crystal layer between the first and second substrates,~~

wherein the first line receives the second signal.
2. (original) The device of claim 1, wherein the switching device includes a thin film transistor.
3. (original) The device of claim 1, wherein the first line includes a gate line.
4. (original) The device of claim 1, wherein the second line includes a data line.
5. (original) The device of claim 1, wherein the first electrodes include a pixel electrode.

6. (original) The device of claim 1, further comprising second electrodes on the first substrate

wherein the second electrodes include a common electrode.

7. (original) The device of claim 6, wherein the plurality of the second electrodes receives the first signal.

8. (cancelled)

9. (original) The device of claim 1, wherein the first auxiliary line is parallel with the first line.

10. (original) The device of claim 1, wherein the second auxiliary line is parallel with the first line.

11. (original) The device of claim 1, wherein the first auxiliary line includes a common line.

12. (currently amended) A liquid crystal display device, comprising:

first and second substrates that are divided in a display area and a non-display area;

a liquid crystal layer between the first and second substrates;

a plurality of switching devices on the first substrate;

first and second lines that apply signals to each switching device;

a plurality of first electrodes on the first substrate;

a first auxiliary line arranged in the non-display area and applying a voltage to the liquid crystal layer, the first auxiliary line receiving a first signal; and

a second auxiliary line arranged in the non-display area, the second auxiliary line receiving a second signal, ~~;~~ and

~~a liquid crystal layer between the first and second substrates;~~

wherein the second signal has a polarity opposite to a polarity of a signal applied to the first line.

13. (original) The device of claim 1, further comprising:

a plurality of pad portions in the non-display area; and

a plurality of electrostatic discharge device in the non-display area.

14. (currently presented) An array substrate for a liquid crystal display device, comprising:

a substrate having a display area and a non-display area;

a liquid crystal layer;

a plurality of switching devices arranged in the display area of the substrate;

first and second lines that apply signals to each switching device;

a plurality of first electrodes on the substrate;

a first auxiliary line arranged in the non-display area and applying a voltage to the liquid crystal layer, the first auxiliary line receiving a first signal; and

a second auxiliary line arranged in the non-display area, the second auxiliary line receiving a second signal,

wherein the first line receives the second signal.

15. (currently amended) An array substrate for a liquid crystal display device, comprising:

a substrate having a display area and a non-display area;

a liquid crystal layer;

a plurality of switching devices arranged in the display area of the substrate;

first and second lines that apply signals to each switching device;

a plurality of first electrodes on the substrate;

a first auxiliary line arranged in the non-display area and applying a voltage to the liquid crystal layer; and

a second auxiliary line arranged in the non-display area;

wherein the second auxiliary line receives a same signal as the first auxiliary line.

16. (original) The array substrate of claim 14, wherein the first line is a gate line.

17. (original) The array substrate of claim 14, wherein the second auxiliary line receives a signal having a polarity periodically opposite to a polarity of a signal received by the first auxiliary line.

18. (original) The arrays substrate of claim 14, further comprising a plurality of second electrodes, wherein the plurality of the second electrodes receive the first signal.

19. (Cancelled)

20. (currently amended) A liquid crystal display device, comprising:

first and second substrates that are divided in a display area and a non-display area;

a liquid crystal layer between the first and second substrates;

a plurality of switching devices on the first substrate;

gate lines and data lines that apply signals to each switching device, the gate lines substantially perpendicular to the data lines;

a plurality of first electrodes on the first substrate;

a first auxiliary line arranged in the non-display area and applying a voltage to the liquid crystal layer, the first auxiliary line receiving a first signal and substantially parallel to the gate lines; and

a second auxiliary line arranged in the non-display area, the second auxiliary line receiving a second signal and substantially parallel to the gate lines, ~~and~~

~~a liquid crystal layer between the first and second substrates.~~

21. (currently amended) An array substrate for a liquid crystal display device, comprising:

a substrate having a display area and a non-display area;

a liquid crystal layer;

a plurality of switching devices arranged in the display area of the substrate;

gate lines and data lines that apply signals to each switching device, the gate lines substantially perpendicular to the data lines;

a plurality of first electrodes on the substrate;

a first auxiliary line arranged in the non-display area and applying a voltage to the liquid crystal layer, the first auxiliary line receiving a first signal and substantially parallel to the gate lines; and

a second auxiliary line arranged in the non-display area, the second auxiliary line receiving a second signal and substantially parallel to the gate lines.